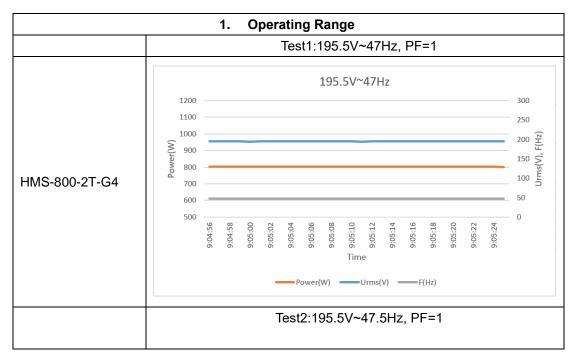
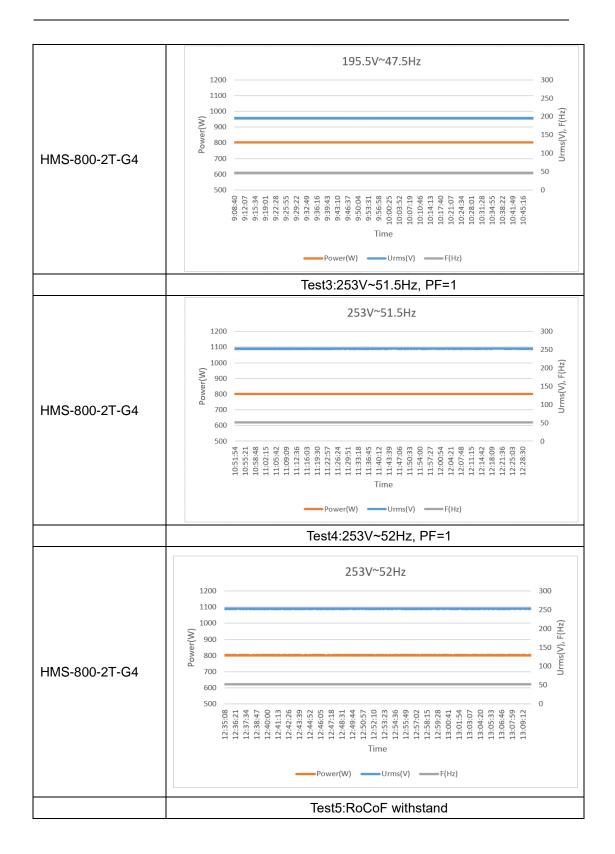
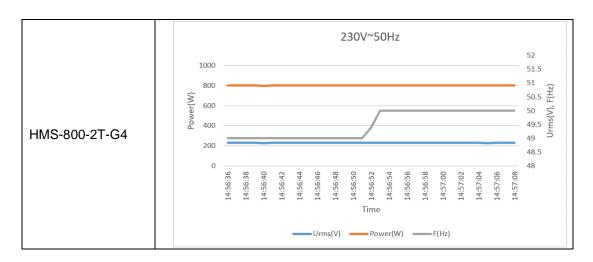
ENA EREC G99/1-4:2019

ENA EREC G99/1-4:2019						
Type Test reference	e number	HMS-800-2T-G4				
Generating Unit te	Generating Unit technology		Photovoltaic Microinverter			
System Supplier na	me	Hoymiles P	ower E	lectronics	Inc.	
Address		No.18 Kang	ijing Ro	oad, Hangz	zhou 310015, China	
Tel	+86 571 2805610	1	Fax		-	
E:mail	info@hoymiles.co	m	Web	site	-	
Registered	0.8 per Unit	kW single p	hase, s	single, spli	t or three phase system	
Capacity, use	NA	kW three ph	nase			
separate sheet if	NA	kW two pha	ses in	three phas	se system	
more than one						
connection	NA	kW two pha	ses sp	lit phase s	ystem	
option.						
Manufacturer com	pliance declaratio	n I certify tl	nat all	products s	supplied by the	
company with the	above Type Teste	d Manufactu	rer's r	eference r	number will be	
manufactured and	l tested to ensure t	hat they per	form a	s stated in	this document, prior	
to shipment to site	and that no site M	Iodifications	are re	quired to e	ensure that the	
product meets all	the requirements o	of EREC G99).			
Signed	结发镰	On behalf o	f	Hoymiles	Power Electronics Inc.	
Note that testing c	an be done by the	Manufactur	er of a	n individua	al component or by	
an external test ho	ouse.					
Where parts of the	Where parts of the testing are carried out by persons or organizations other than the					
Manufacturer ther	that person or org	ganization sh	all kee	p copies	of all test records	
and results supplie	and results supplied to them to verify that the testing has been carried out by people					
	nical competency		-			
<u>L</u>	. ,	2				



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	2. Power Quality - Harmonic Generation Generating Unit tested to BS EN 61000-3-12										
	G	enerating Unit	tested to BS	=N 61000-3-12	1	0/					
Generating Unit rating per phase				Harmonic % =Measured Value							
(rpp)		porpriace	0.8	kW	(Amps) x	23/rating					
					per phase	e (kVA)					
Harmo nic	At 45-55% o	f rated output	100% of ra	ated output		BS EN 0-3-12					
	Measured		Measured								
	Value MV	0/	Value MV	0/	4.1	. .					
	in Amps	%	in Amps	%	1 phase	3 phase					
2	0.0202	0.5808	0.0195	0.5606	8%	8%					
0	0.0123	0.3536	0.0114	0.3278	04.00/	Not					
3					21.6%	stated					
4	0.0092	0.2645	0.0094	0.2703	4%	4%					
5	0.0156	0.4485	0.0156	0.4485	10.7%	10.7%					
6	0.0054	0.1553	0.0061	0.1754	2.67%	2.67%					
7	0.0213	0.6124	0.0219	0.6296	7.2%	7.2%					
8	0.0055	0.1581	0.0058	0.1668	2%	2%					
9	0.0152	0.437	0.0151	0.4341	3.8%	Not					
9					5.0 %	stated					
10	0.0034	0.0978	0.0039	0.1121	1.6%	1.6%					
11	0.0086	0.2473	0.0083	0.2386	3.1%	3.1%					
12	0.0024	0.069	0.0025	0.0719	1.33%	1.33%					
13	0.0081	0.2329	0.008	0.23	2%	2%					
THD		2.4154		2.416	23%	13%					
PWHD		4.6016		4.6018	23%	22%					

3. Power Quality. Voltage fluctuations and Flicker

Test to BS EN 61000-3-11											
	Startin	g	1	Stoppi	ng	[Running				
	dmax [%]	dc [%]	d(t) [%]	dmax [%]	dc [%]	d(t) [%]	Pst	Plt 2 hours			
Measured Values											
at	0.1	0	0	0.1	0	0	0.066	0.066			
test impedance											
Normalized to standard impedance	0.1	0	0	0.1	0	0	0.066	0.066			
Normalized to											
required maximum	0.1	0	0	0.1	0	0	0.066	0.066			
impedance											
Limits set under BS EN 61000-3- 11	4%	3.3%	3.3%	4%	3.3%	3.3%	1	0.65			
Test impedance	R	0.4		Ω	XI	0.25		Ω			
Standard	R	0.24*		0	XI	0.15*					
impedance	К	0.4^		32		0.25^		Ω			
Maximum impedance	R	0.4		Ω	ХІ	0.25		Ω			
				Test							
Test start date	Test start date		2024-02-28		2024-02-28						
				date							
Test location							N INSTITU CO., LTD				

4. Power quality. DC injection						
Test power level	10%	55%	100%			
Recorded value(mA)	0.443	1.059	1.898			
as % of rated AC	0.0107	0.0204	0.0540			
current	0.0127	0.0304	0.0546			
Limit	0.25%	0.25%	0.25%			

5. Power Quality. Power factor									
	216.2V	230V	253V	Measured at three voltage levels					
Measured value	0.9984	0.9988	0.9979	and at full output. Voltage to be maintained within ±1.5% of the					
Limit	>0.95	>0.95	>0.95	stated level during the test.					

	6. Protection. Frequency tests										
Function	Setting		Trip test		"No trip tests"						
	Frequency	Time	Frequency	Time	Frequency /time	Confirm					
	riequency	delay	Trequency	delay	r requericy / lime	no trip					
U/F stage 1	47.5Hz	20s	47.5Hz	20.01s	47.7Hz/30s	Confirmed					
U/F stage 2	47Hz	0.5s	47Hz	0.51s	47.2Hz/19.5s	Confirmed					
					46.8Hz/0.45s	Confirmed					
O/F stage 1	52Hz	0.5s	52Hz	0.51s	51.8Hz/120s	Confirmed					
					52.2Hz/0.45s	Confirmed					

	7. Protection. Voltage tests									
Function	Setting		Trip test		"No trip tests"					
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip				
U/V stage 2	184V	2.5s	183.8V	2.51s	188V/5.0s	Confirmed				
					180V/2.45s	Confirmed				
OV stage 1	262.2V	1.0s	262.5V	1.01s	258.2V/5.0s	Confirmed				
O/V stage 2	273.7V	0.5s	274.1V	0.51s	269.7V/0.95s	Confirmed				
		277.7V/0.45s	Confirmed							

8. Power Park Modules - Protection - Loss of Mains test									
Note: Inverter tested according to BS EN 62116.									
Teet Dewer and	33%	66%	100%	33%	66%	100%			
Test Power and	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P			
imbalance	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10			
Trip time. Limit is 0.5s	103.6ms	221.6ms	283.1ms	103.6ms	215.6ms	276.1ms			

9. Loss of Mains Protection, Vector Shift Stability test and RoCoF Stability test									
	Start	Change	End	Confirm no trip					
	Frequency	Change	Frequency	Commune unp					
Positive Vector Shift	49.5Hz	+50 degrees		Confirmed					
Negative Vector Shift	50.5Hz	- 50 degrees		Confirmed					
	Down rongo	Test frequency	Test	Confirm no trin					
	Ramp range	ramp	Duration	Confirm no trip					
Positive Frequency drift	49Hz to 51Hz	+0.95Hz/sec	2.1s	Confirmed					
Negative Frequency drift	51Hz to 49Hz	-0.95Hz/sec	2.1s	Confirmed					

10. Limited Frequency Sensitive Mode - Over frequency test								
Active Power resp	Active Power response to rising frequency/time plots are attached if							
frequency injection	n tests are undertak	en in accordance w	vith Annex A.8.2.4	N				
Test sequence	Measured Frequency Primary Act							
at Registered	Active	Active (Hz) Power						
Capacity >80	Power Output		Source	Gradient				
%	(W)							
Step a) 50.00Hz	000.05	200.05 50						
±0.01Hz	802.85	50						

Step b) 50.45Hz	704.00			-
±0.01Hz	794.93	50.45		
Step c) 50.70Hz	750.00	F0 7		-
±0.10Hz	756.23	50.7		
Step d) 51.15Hz	000 40			-
±0.05Hz	686.12	51.15		
Step e) 50.70Hz	750 47	50.7		-
±0.10Hz	756.47	50.7		
Step f) 50.45Hz	705.05	50.45		-
±0.05Hz	795.05	50.45		
Step g) 50.00Hz	802.25	50		-
±0.01Hz	002.25	50		
Test sequence	Measured	Frequency	Primary	Active
at Registered	Active	(Hz)	Power	Power
Capacity	Power Output		Source	Gradient
40%~60%	(W)			
Step a) 50.00Hz	403.12	50		-
±0.01Hz	403.12	50		
Step b) 50.45Hz	396.11	50.45		-
±0.05Hz	590.11	50.45		
Step c) 50.70Hz	356.52	50.7		-
±0.10Hz	550.52	50.7		
Step d) 51.15Hz	282.39	51.15		-
±0.05Hz	202.39	51.15		
Step e) 50.70Hz	356.02	50.7		-
±0.10Hz	330.02	50.7		
Step f) 50.45Hz	395.85	50.45		
±0.05Hz	090.00	50.45		
Step g) 50.00Hz	403.03	50		
±0.01Hz	-00.00	5		

	11. Protection. Re-connection timer								
	Test should prove that the reconnection sequence starts in no less than 20s for restoration								
of voltage and f	requency to wit	hin the stage 1	settings of table	e 10.5.7.1					
Time delay	Measured	Checks on no	reconnection v	when voltage or	frequency is				
setting	delay	brought to jus	st outside stage	1 limits of table	10.5.7.1.				
20s	30.1s	At 266.2V	At 180V	At 47.4Hz	At 52.1Hz				
Confirmation th	Confirmation that the								
Generating Unit does not re- Confirmed Confirmed Confirmed Confirmed									
connect.									

12. Fault level contribution						
For machines with electro-magnetic output		For Inverter output				
Parameter	Symbol	Value	Time after fault	Volts	Amps	

Peak Short Circuit current	ір	N/A	20ms	19.17V	0.475A
Initial Value of aperiodic current	А	N/A	100ms	11.7V	0.224A
Initial symmetrical short-circuit current*	lk	N/A	250ms	9.47V	0.109A
Decaying (aperiodic) component of short circuit current*	iDC	N/A	500ms	5.48V	0.088A
Reactance/Re sistance Ratio of source*	X/R	N/A	Time to trip	0.004s	(in seconds)
For rotating machines and linear piston machines the test should produce a 0s – 2s plot of the short circuit current as seen at the Generating Unit terminals. * Values for these parameters should be provided where the short circuit duration is					

sufficiently long to enable interpolation of the plot

13. Self-Monitoring solid state switching	Yes/or NA
It has been verified that in the event of the solid-state switching device failing to disconnect the Generating Unit , the voltage on the output side of the switching device is reduced to a value below 50 Volts within 0.5 seconds	N/A

14. Wiring functional tests: If required by para 15.2.1		
Confirm that the relevant test schedule is attached (tests to be undertaken		
at time of commissioning).	N/A	

15. Logic interface (input port)	Yes/or NA	
Confirm that an input port is provided and can be used to shut down the	Yes	
module.	163	